

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Toshinari Takayanagi

Title: NEGATIVE BIAS TEMPERATURE INSTABILITY (NBTI)  
PRECONDITIONING OF MATCHED DEVICES

Application No.: 10/664,104

Filed: September 17, 2003

Examiner: Not Yet Assigned

Group Art Unit: 2811

Atty. Docket No.: 004-9196

July 9, 2004

Mail Stop Amendment  
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Alexandria, VA 22313-1450**INFORMATION DISCLOSURE STATEMENT**  
**37 C.F.R. § 1.97(b)**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO-1449 (1 pages), including copy(ies) of 1 reference(s).  
☒ Other: List of Pending U.S. Patent Applications, including copies of 3 applications.

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

1. an admission that the information necessarily is, or corresponds to, prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described below; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

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For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance through (i) an English language abstract, (ii) an English language equivalent application, or (iii) if cited in a search report or other action

by a foreign patent office in a counterpart foreign application, an English language version of the search report or action that indicates the degree of relevance found by the foreign office.

### FEE AUTHORIZATION

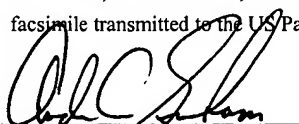
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- ☒ The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

If however, this Information Disclosure Statement is filed after the period specified in § 1.97(b), the undersigned hereby authorizes the Commissioner to charge the fee set forth in § 1.17(p) to Deposit Account No. 50-0631.

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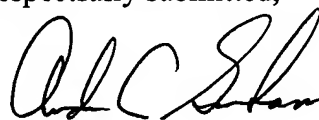
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Andrew C. Graham

7/9/09  
Date

Respectfully submitted,



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U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 004-9196		
				Application No.: 10/664,104		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s): Toshinari Takayanagi		
(Use several sheets if necessary)				Filing Date: September 17, 2003		
				Group Art Unit: 2811		
				Date Submitted: July 9, 2004		
U.S. Patent Documents						
*Examiner Initial		Document Number	Date	Name		
	AA	6,574,160	06-03-2003	Nadeem N. Eleyan et al.		
	AB	6,470,467	10-22-2002	Tomishima et al.		
	AC	6,442,644	08-27-2002	Gustavson et al.		
	AD	6,359,816	03-19-2002	Nurser, Henry		
	AE	6,185,712	02-06-2001	Kiriata et al.		
	AF	6,111,796	08-29-2000	Chang et al.		
	AG	6,072,733	06-06-2000	Advani		
	AH	6,041,419	03-21-2000	Huang et al.		
	AI	6,038,195	03-14-2000	Farmwald et al.		
	AJ	6,005,824	12-21-1999	Crafts		
	AK	5,796,993	08-18-1998	Maguire		
	AL	5,764,178	06-09-1998	Kim		
	AM	5,638,333	06-10-1997	Lee		
	AN	US 2003/0198112 A1	10-23-2003	Nadeem N. Eleyan et al.		
Foreign Patent Documents						
					Translation	
		Document	Date	Country	Yes	No
	AO	FR 2 792 760	Oct. 27, 2000	France		X
	AP					
Examiner		Date Considered				
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.						



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### **LIST OF PENDING U.S. PATENT APPLICATIONS**

The following pending U.S. patent applications are listed for consideration by the Examiner.

- 1) U. S. Patent Application No. 10/664,121, filed September 17, 2003, entitled "Negative Bias Temperature Instability (NBTI) Hardened Sense Amplifier Circuit and Method Therefor," naming inventor, Toshinari Takayanagi.
- 2) U. S. Patent Application No. 10/683,636, filed October 10, 2003, entitled "Test Circuit for Measuring Sense Amplifier and Memory Mismatches," naming inventors, Nadeem N. Eleyan, Howard L. Levy and Jeffrey Y. Su.
- 3) U. S. Patent Application No. 10/683,633, filed October 10, 2003, entitled "Measuring and Correcting Sense Amplifier and Memory Mismatches Using NBTI," naming inventors, Nadeem N. Eleyan, Howard L. Levy and Jeffrey Y. Su.

***Applicant(s) are providing a separate list of pending applications, pursuant to MPEP § 609III(D), to avoid publication of the application numbers on any patent that issues from the above-identified application. Applicant(s) request that the Examiner indicate consideration of the pending applications listed above in accordance with MPEP 609III(C)(2) and return a copy of this page with the next communication.***